ABSTRACT OF THE DISCLOSURE

A method of fault correction for an array of fusible links includes the steps of storing digital information in a first plurality of fusible links to place the first plurality of fusible links in a first programmed state; storing the digital information in a second plurality of fusible links to place the second plurality of fusible links in a second programmed state; and performing a logic operation to combine the first programmed state with the second programmed state, the logic operation providing an output that is identical to one of the first programmed state and the second programmed state for a respective one of the first plurality of fusible links and the second plurality of fusible links that does not include a faulty bit.

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